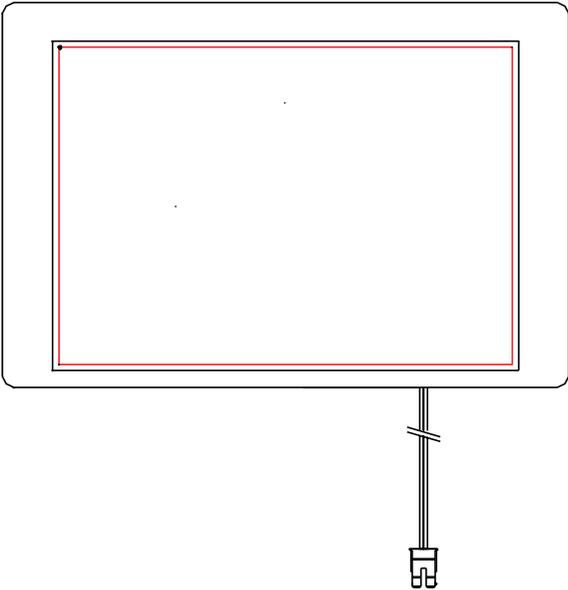




PRODUCT SPECIFICATION

# HDA570V-GHL

VGA(640X480) , TFT COLOR GRAPHICS  
LCD DISPLAY MODULE



HANTRONIX, INC. 10080 BUBB RD. CUPERTINO, CA 95014	Q.A.:	REV.:	HDA570V-GHL	SHEET 1 OF 19
	Z.W.	1.0		DATE: 10/29/10

# 1 General Description and Features

HDA570V-GHL is a transmissive type color active matrix TFT (Thin Film Transistor) liquid crystal display (LCD) that uses amorphous silicon TFT as a switching device. This model is composed of a TFT-LCD module, a driver circuit and a back-light unit. Graphics and texts can be displayed on a VGA 640 (W) x 3 x 480 (H) dots with 262,144 colors by supplying 18 L<sup>its</sup> data signal (6bits/each color).

## 1.1 Features

- ◆ Transmissive and back-light with 30 LEDs are available
- ◆ TN (Twisted Nematic) mode.
- ◆ Digital RGB (6bits/color) data transfer

## 1.2 LCD Module

Item	Specification	Unit
Screen Size	5.7 inches	Diagonal
Display Resolution	640 (H) x 480 (V)	Pixel
Active Area	115.20 (H) x 86.40 (V)	mm
Outline Dimension	144.00 (H) x 104.60 (V) x 13.0 (T)	mm
Display Mode	Normally white mode/ Transmissive/ Wide view	--
Pixel Arrangement	R,G,B Vertical Tripe	--
Pixel size	181.5 x 181.5	um
Display Color	262K Colors	--
Viewing Direction	6 o'clock	--
Input Interface	LVDS (6bits/color) Data Transfer	--

## 2 Mechanical Information

Item		Min.	Typ.	Max.	Unit	Note
Module Size	Horizontal (H)	--	144.00	--	mm	(1)
	Vertical (V)	--	104.60	--	mm	(1)
	Thickness (T)	--	13.0	--	mm	(1)
Weight		--	154	--	g	--

Note (1) Excluding backlight cables. . Refer to the Outline Dimension Drawing as attached.

HANTRONIX, INC.  
10080 BUBB RD.  
CUPERTINO, CA 95014

Q.A.:  
Z.W.

REV.:  
1.0

HDA570V-GHL

SHEET 3 OF 19

DATE:  
10/29/10

### 3 Electrical Specifications

#### 3.1 Absolute Max. Ratings

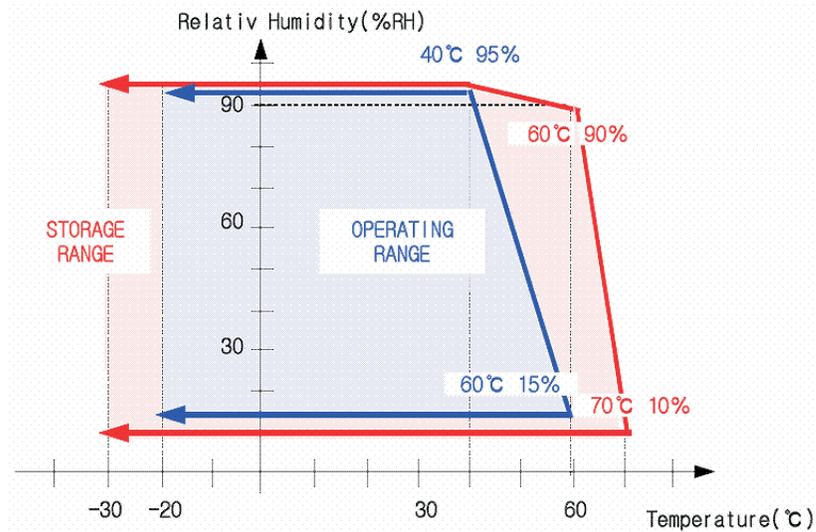
##### 3.1.1 Absolute Ratings of Environment

If the operating condition exceeds the following absolute maximum ratings, the TFT LCD module may be damaged permanently.

( $T_a = 25 \pm 2^\circ\text{C}$ ,  $V_{SS} = \text{GND} = 0$ )

Item	Symbol	Min.	Max.	Unit	Note
Storage temperature	$T_{STG}$	-30	80	$^\circ\text{C}$	(1)
Operating temperature	$T_{OPR}$	-20	70	$^\circ\text{C}$	(1,2,3)

Note (1) 95 % RH Max. ( $40^\circ\text{C} \geq T_a$ ). Maximum wet-bulb temperature at  $39^\circ\text{C}$  or less. ( $T_a > 40^\circ\text{C}$ ) No condensation.



Note (2) In case of below  $0^\circ$ , the response time of liquid crystal (LC) becomes slower and the color of panel becomes darker than normal one. Level of retardation depends on temperature, because of LC's character

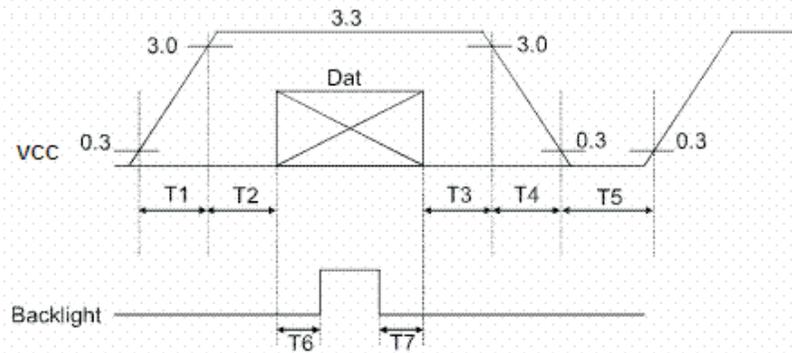
Note (3) Only operation is guaranteed at operating temperature. Contrast, response time, another display quality are evaluated at  $+25^\circ\text{C}$ .

### 3.1.2 Electrical Absolute Maximum Ratings

(V<sub>SS</sub>=GND=0)

Parameter	Symbol	Min.	Max.	Unit	Remark
Power supply voltage	V <sub>CC</sub>	-0.3	5.0	V	
Signal input voltage	R0-R5,G0-G5, B0-B5,DCLK,DE	-0.3	V <sub>CC</sub> +0.3	V	--
Permissive input ripple voltage	V <sub>RF</sub>	--	100	mVp-p	V <sub>CC</sub> = +3.3V

Display On/Off Sequence :



Data: DCLK, R0 ~ R5, G0 ~ G5, B0 ~ B5, DE

T1≤10ms, 50ms≤T2, 0<T3≤50ms, 0<T4≤10ms, 1s≤T5, 200ms≤T6, 200ms≤T7

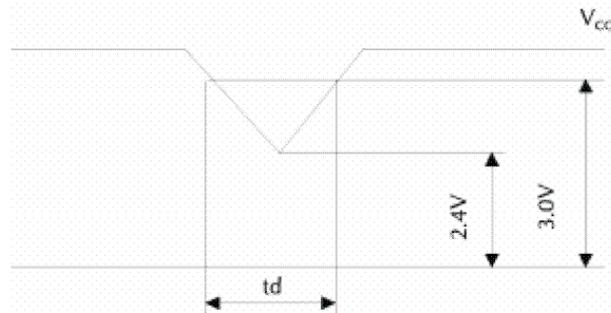
### 3.2 Electrical Characteristics

#### 3.2.1 DC Electrical Characteristics of the TFT LCD

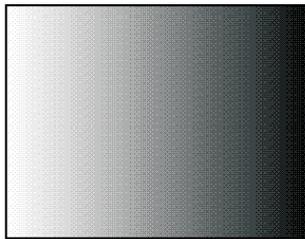
(Ta=25±2°C, V<sub>SS</sub>=GND=0)

Item	Symbol	Min.	Typ.	Max.	Unit	Remark
Power supply	VCC	3.0	3.3	3.6	V	Note 1
Input Voltage for logic	H Level	V <sub>IH</sub>	0.7V <sub>DD</sub>	-	V <sub>DD</sub>	V
	L Level	V <sub>IL</sub>	0	-	0.3V <sub>DD</sub>	V
Power Supply current	ICC		(120)	TBD	mA	Note 2

Note1: V<sub>CC</sub>-dip conditions  
 V<sub>CC</sub>-dip conditions should also follow the V<sub>CC</sub>-turn-on conditions  
 T<sub>d</sub> ≤ 10ms



Note2: f<sub>v</sub> =60Hz , Ta=25°C , Display pattern : 64 Gray pattern



### 3.3 AC Timing Characteristic of The LCD

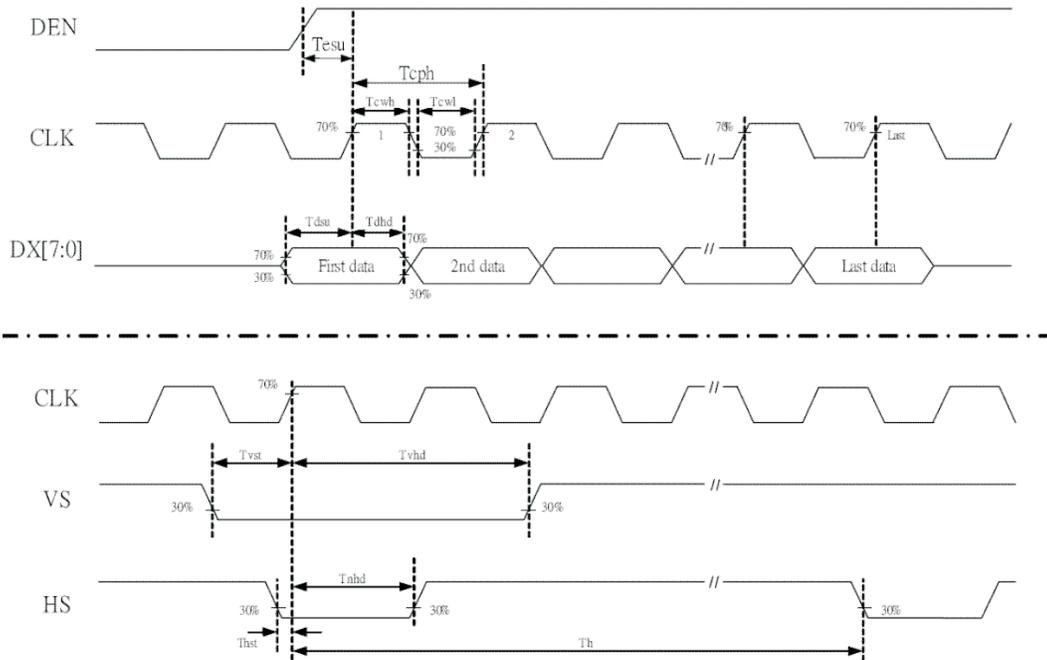
#### 3.3.1 Timing Condition

Parameter	Symbol	Min.	Typ.	Max.	Unit.	Remark
CLK frequency	$F_{CPH}$	--	25.175	--	MHZ	
CLK period	$T_{CPH}$	--	39.7	--	ns	
CLK pulse duty	$T_{CWH}$	40	50	60	%	
HS period	$T_H$	--	800	--	$T_{CPH}$	
HS pulse width	$T_{WH}$	5	30	--	$T_{CPH}$	
HS-DEN time	$T_{HS}$	112	144	175	$T_{CPH}$	
DEN pulse width	$T_{EP}$	--	640	--	$T_{CPH}$	
VS pulse width	$T_{WV}$	1	3	5	$T_H$	
VS-DEN time	$T_{STV}$	--	35	--	$T_H$	
VS period	$T_V$	--	525	--	$T_H$	

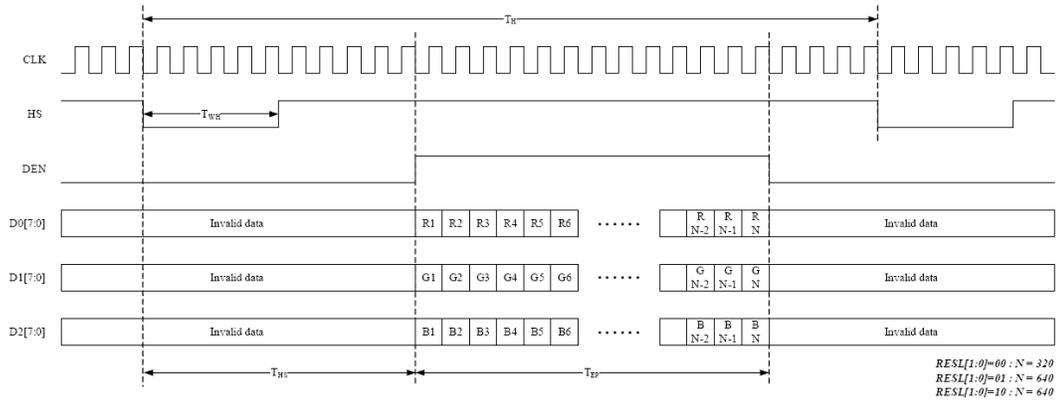
Note : When SYNC mode is used, 1st data start from 144th CLK after HS falling (when  $STHD[5:0]=00000$ )

Parameter	Symbol	Min.	Typ.	Max.	Unit.	Remark
OEV pulse width	$T_{CvE}$	--	100	--	$T_{CPH}$	
CKV pulse width	$T_{CKV}$	--	96	--	$T_{CPH}$	
HS-CKV time	$T_1$	--	52	--	$T_{CPH}$	
HS-OEV tim	$T_2$	--	8	--	$T_{CPH}$	
HS-POL time	$T_3$	--	72	--	$T_{CPH}$	
STV setup time	$T_{SUV}$	--	46	--	$T_{CPH}$	
STV pulse width	$T_{WSTV}$	--	1	--	$T_H$	

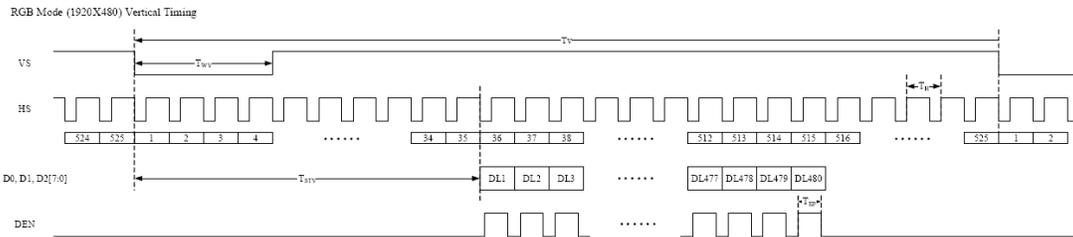
### 3.3.2 Clock and Data input waveforms



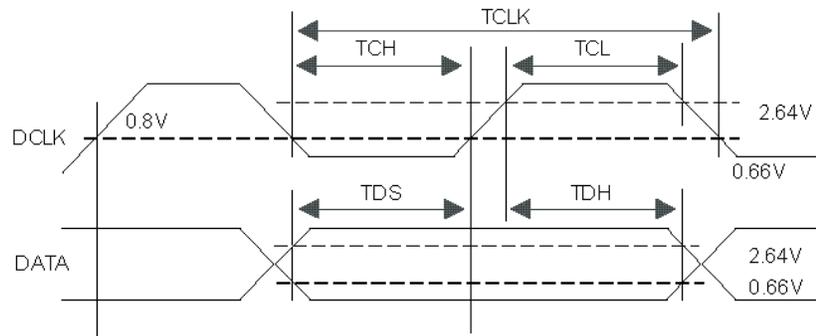
### 3.3.3 Data input format for parallel RGB Mode



### 3.3.4 Digital RGB mode Horizontal timing for RESL[1:0]=10

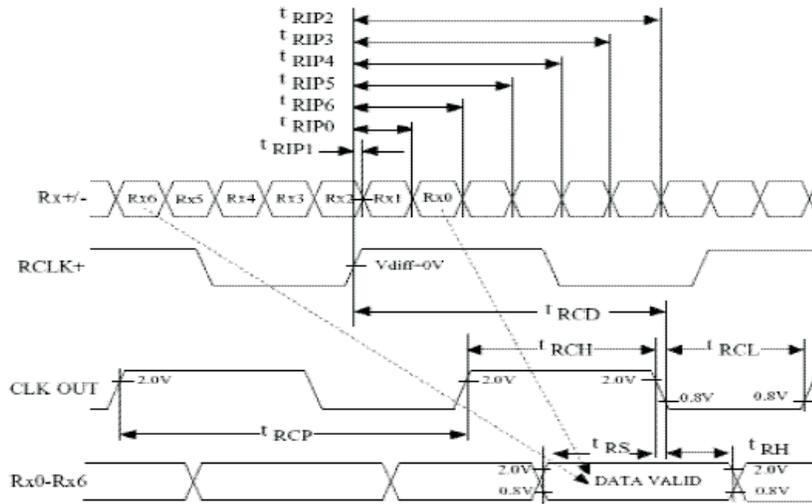


HANTRONIX, INC. 10080 BUBB RD. CUPERTINO, CA 95014	Q.A.:	REV.:	HDA570V-GHL	SHEET 8 OF 19
	Z.W.	1.0		DATE:



### 3.3.5 LVDS Timing Condition

$t_{RCP}$	CLK OUT Period	11.76	T	50.0	ns
$t_{RCH}$	CLK OUT High Time		$4T/7$		ns
$t_{RCL}$	CLK OUT Low Time		$3T/7$		ns
$t_{RCD}$	RCLK+/- to CLK OUT Delay		$5T/7$		ns
$t_{RS}$	TTL Data Setup to CLK OUT	$3T/7-2.5$			ns
$t_{RH}$	TTL Data Hold from CLK OUT	$4T/7-3.5$			ns
$t_{TLH}$	TTL Low to High Transition Time		3.0	5.0	ns
$t_{THL}$	TTL High to Low Transition Time		3.0	5.0	ns
$t_{RIP1}$	Input Data Position 0 (T=11.76ns)	-0.4	0.0	0.4	ns
$t_{RIP0}$	Input Data Position 1 (T=11.76ns)	$T/7-0.4$	$T/7$	$T/7+0.4$	ns
$t_{RIP6}$	Input Data Position 2 (T=11.76ns)	$2T/7-0.4$	$2T/7$	$2T/7+0.4$	ns
$t_{RIP5}$	Input Data Position 3 (T=11.76ns)	$3T/7-0.4$	$3T/7$	$3T/7+0.4$	ns
$t_{RIP4}$	Input Data Position 4 (T=11.76ns)	$4T/7-0.4$	$4T/7$	$4T/7+0.4$	ns
$t_{RIP3}$	Input Data Position 5 (T=11.76ns)	$5T/7-0.4$	$5T/7$	$5T/7+0.4$	ns
$t_{RIP2}$	Input Data Position 6 (T=11.76ns)	$6T/7-0.4$	$6T/7$	$6T/7+0.4$	ns
$t_{RPLL}$	Phase Lock Loop Set			10.0	ms



### 3.3.6 LVDS Data Mapping

	TA0	TA1	TA2	TA3	TA4	TA5	TA6	TB0	TB1	TB2	TB3	TB4	TB5	TB6	TC0	TC1	TC2	TC3	TC4	TC5	TC6
6BIT	R0	R1	R2	R3	R4	R5	G0	G1	G2	G3	G4	G5	B0	B1	B2	B3	B4	B5	HS	VS	DE

### 3.4 Back-Light Unit

The Back-light system is an edge-lighting type with 30 white LEDs(Light Emitting Diode). The characteristics of 30 white LEDs are shown in the following tables.

(Ta= Room Temp)

Characteristics	Symbol	Min.	Typ.	Max.	Unit	Note
Current of Back-light Unit	$I_B$	-	200	TBD	mA	(1)
Voltage of Back-light Unit	$V_B$	-	(9.6)		V	
Power Consumption	$P_{BL}$	-	(1920)		mW	(2)
LED Life Time	25°C	-	(30000)		hr	(3)

Note (1) LEDs in 3 series x 10 parallel type.

(2) Where  $I_B = 200\text{mA}$ ,  $V_B = 9.6$ ,  $P_{BL} = V_B \times I_B$

(3) The environmental conducted under ambient air flow ,at  $T_a = 25 \pm 2^\circ\text{C}$ ,  $60\%RH \pm 5\%$

HANTRONIX, INC. 10080 BUBB RD. CUPERTINO, CA 95014	Q.A.:	REV.:	HDA570V-GHL	SHEET 10 OF 19
	Z.W.	1.0		DATE: 10/29/10

## 4 Optical Characteristics

### 4.1 Optical characteristic of the LCD

The following items are measured under stable conditions. The optical characteristics should be measured in a dark room or equivalent state.

Measuring equipment: BM-5A ,BM-7

Item	Symbol	Condition	Min	Typ	Max	Unit	Note
Brightness			700	(900)	--	cd/m <sup>2</sup>	
Response time	T <sub>r</sub>	θ=0°	-	15	20	ms	
	T <sub>f</sub>		--	25	35	ms	
Contrast ratio	CR	At optimized viewing angle	400	(500)	--	--	
Color Gamut	NTSC %	--	--	50	--	%	
Color Chromaticity (CIE 1931)	Red	R <sub>x</sub>	θ=0° Normal Viewing Angle	0.565	0.615	0.665	--
		R <sub>y</sub>		0.294	0.344	0.394	
	Green	G <sub>x</sub>		0.257	0.307	0.357	--
		G <sub>y</sub>		0.512	0.562	0.612	
	Blue	B <sub>x</sub>		0.090	0.140	0.190	--
		B <sub>y</sub>		0.080	0.130	0.180	
	White	W <sub>x</sub>		0.259	0.309	0.359	--
		W <sub>y</sub>		0.300	0.350	0.400	
Viewing Angle (6H)	Hor.	θ <sub>R</sub>	CR≥10	55	65	--	Degree
		θ <sub>L</sub>		55	65	--	
	Ver.	φ <sub>H</sub>		55	55	--	
		φ <sub>L</sub>		40	65	--	

HANTRONIX, INC.  
10080 BUBB RD.  
CUPERTINO, CA 95014

Q.A.:  
Z.W.

REV.:  
1.0

HDA570V-GHL

SHEET 11 OF 19

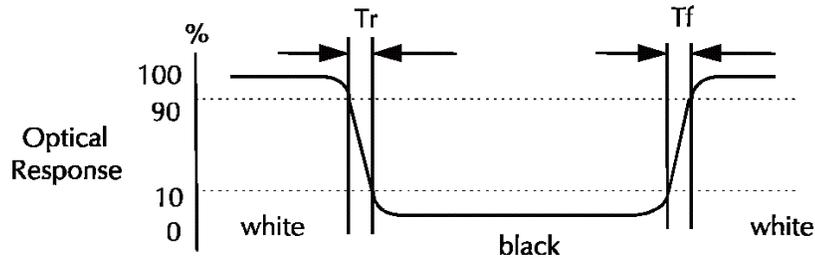
DATE:  
10/29/10

a. Test equipment setup

After stabilizing and leaving the panel alone shall be warmed up for the stable operation of LCM, the measurement should be executed. Measurement should be executed in a stable, windless, and dark room. Optical specifications are measured by Topcon BM-7(fast) with a viewing angle of 2° at a distance of 50cm and normal direction.

b. Definition of response time: Tr and Tf

The response time is defined as the following figure and shall be measured by switching the input signal for "black" and "white".



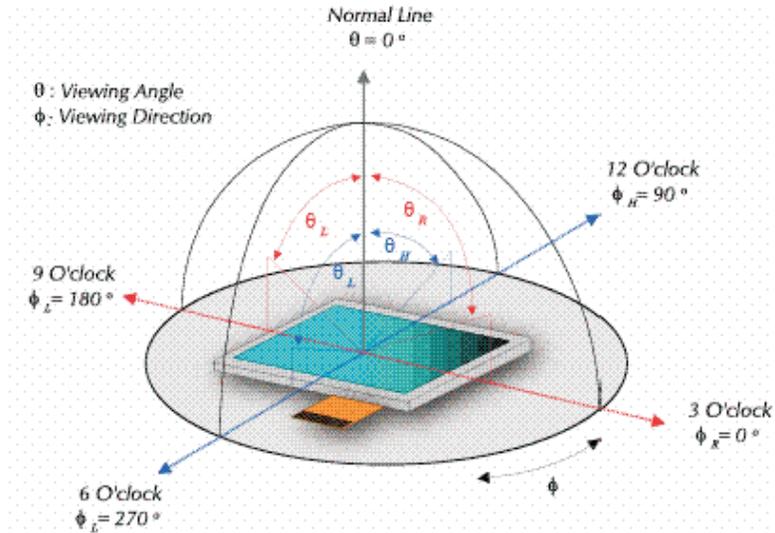
c. Definition of contrast ratio:

$$\text{Contrast Ratio (CR)} = \frac{\text{Brightness measured when LCD is at "white state"}}{\text{Brightness measured when LCD is at "black state"}}$$

d. Measured at the center area of the panel when all the input terminals of LCD panel are electrically opened.

HANTRONIX, INC. 10080 BUBB RD. CUPERTINO, CA 95014	Q.A.:	REV.:	HDA570V-GHL	SHEET 12 OF 19
	Z.W.	1.0		DATE: 10/29/10

e. View Angle



f. Definition of Luminance of White: Luminance of white at the center points

Light Source of Back-Light Unit	LED Type
---------------------------------	----------

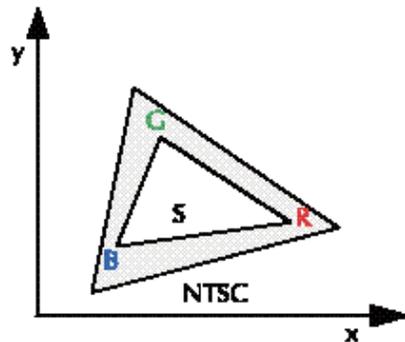
g. Definition of White Uniformity

$$\text{White Uniformity} = \frac{\text{Min. luminance of white among 9-points}}{\text{Max. luminance of white among 9-points}}$$

h. The definition of Color Gamut -Color Chromaticity CIE 1931

Color coordinate of white & red, green, blue at center point.

Color Gamut : NTSC(%) = ( RGB Triangle Area / NTSC Triangle Area ) x 100

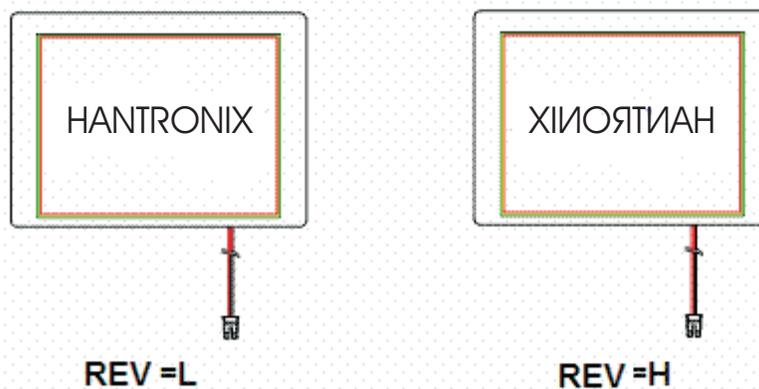


## 5 Input Terminal Pin Assignment

### 5.1 Pin Assignment

Pin No.	Symbol	I/O	Function	Remark
1	VCC	P	Power Supply 3.3V	
2	VCC	P	Power Supply 3.3V	
3	VSS	P	Ground	
4	VSS	P	Ground	
5	RXIND-	I	LVDS Data Differential Pair 0 input	
6	RXIND+	I	LVDS Data Differential Pair 0 input	
7	VSS	P	Ground	
8	RXIN1-	I	LVDS Data Differential Pair 1 input	
9	RXIN1+	I	LVDS Data Differential Pair 1 input	
10	VSS	P	Ground	
11	RXIN2-	I	LVDS Data Differential Pair 2 input	
12	RXIN2+	I	LVDS Data Differential Pair 2 input	
13	VSS	P	Ground	
14	RXCLK-	I	LVDS Clock Differential input	
15	RXCLK+	I	LVDS Clock Differential input	
16	VSS	P	Ground	
17	NC	-	NC	
18	NC	-	NC	
19	REV	I	Select horizontal and vertical scanning direction.	Note 1
20	VSS	P	Ground	

Note 1:



HANTRONIX, INC.  
10080 BUBB RD.  
CUPERTINO, CA 95014

Q.A.:  
Z.W.

REV:  
1.0

HDA570V-GHL

SHEET 14 OF 19

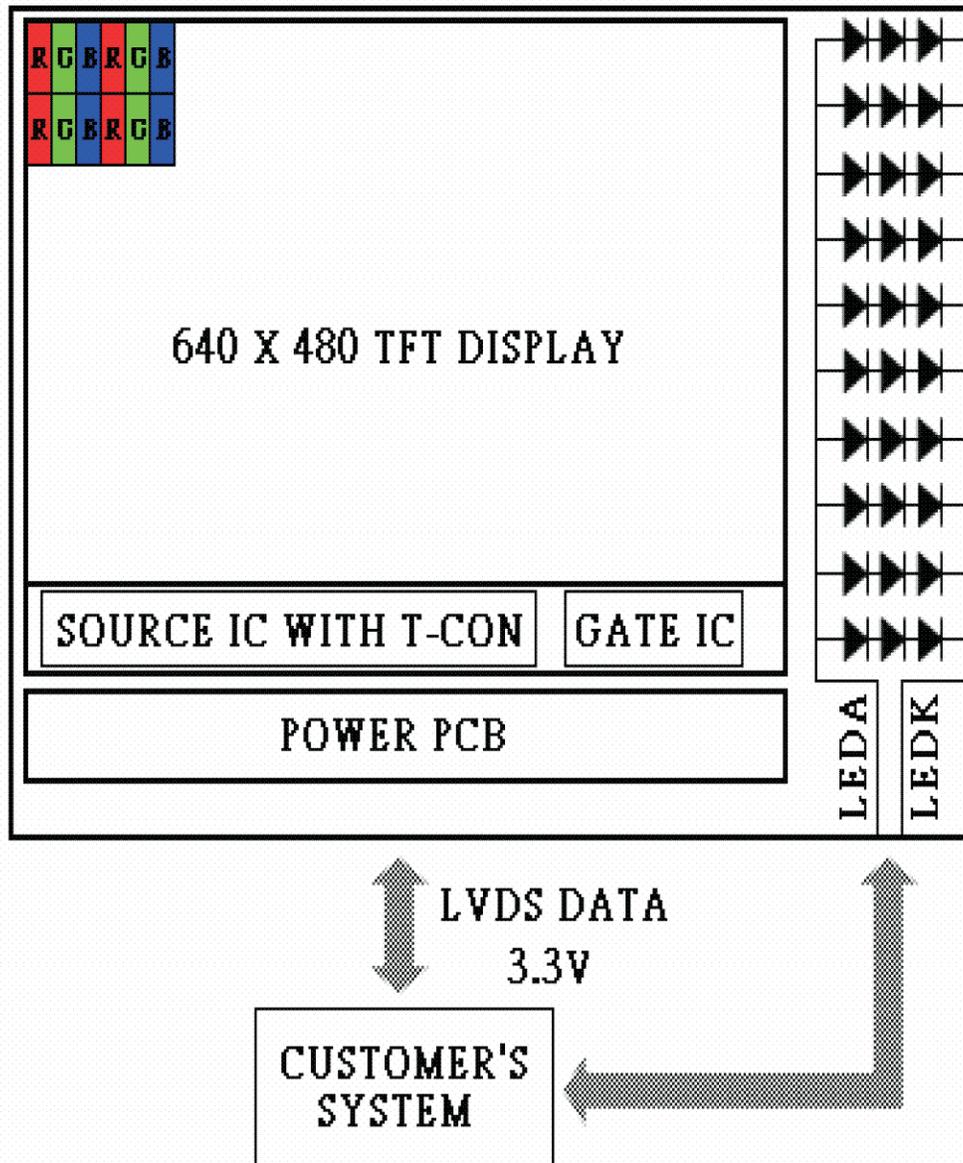
DATE:  
10/29/10

## 5.2 Back-light Unit (BLU)

Pin No.	Symbol	Function	Remark
1	LEDA	Power Supply for LED backlight	Red
2	LEDK	GND for LED backlight	Black

Connector: JST BHSR-02VS-1

### 5.3 Block Diagram



## 5.4 Basic Display Color and Gray Scale

	Color & Gray Scale	Data Signal																							
		R7	R6	R5	R4	R3	R2	R1	R0	G7	G6	G5	G4	G3	G2	G1	G0	B7	B6	B5	B4	B3	B2	B1	B0
Basic Color	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
	Red(0)	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	
	Green(0)	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	
	Blue(0)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	
	Cyan	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	
	Magenta	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	
	Yellow	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	
	White	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	
Red	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0		
	Red(1)	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0		
	Red(2)	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0		
	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:		
	Red(31)	0	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0		
	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:		
	Red(62)	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0		
	Red(63)	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0		
Green	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0		
	Green(1)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0		
	Green(2)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0		
	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:		
	Green(31)	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	0	0	0	0	0	0		
	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:		
	Green(62)	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	0	0	0	0	0	0	0		
	Green(63)	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	0	0	0	0	0	0	0		
Blue	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0		
	Blue(1)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1		
	Blue(2)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1		
	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:		
	Blue(31)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1		
	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:		
	Blue(62)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	0		
	Blue(63)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1		

0 : Low level voltage, 1 :High level voltage

Each basic color can be displayed in 64 gray scales from 6 bit data signals. With the combination of total 18 bit data signals, the 262,144-color display can be achieved on the screen.

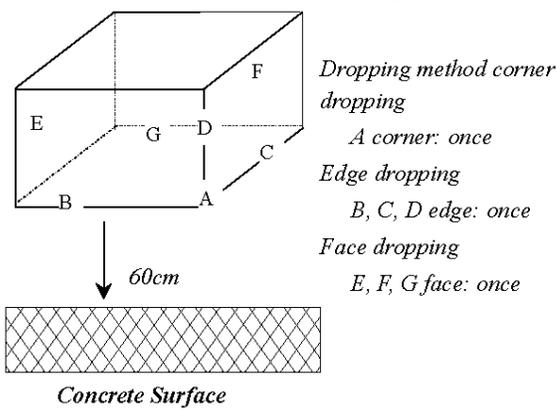
## 6 Reliability Condition

No change on display and in operation under the following test condition.

Condition: Unless otherwise specified, tests will be conducted under the following condition.

Temperature:  $20\pm 5^{\circ}\text{C}$  Humidity:  $65\pm 5\%\text{RH}$

Tests will be not conducted under functioning state.

No.	Parameter	Condition	Notes
1	High Temperature Operating	$70^{\circ}\text{C}\pm 2^{\circ}\text{C}$ , 240hrs (Operation state)	-
2	Low Temperature Operating	$-20^{\circ}\text{C}\pm 2^{\circ}\text{C}$ , 240hrs (Operation state)	-
3	High Temperature Storage	$80^{\circ}\text{C}\pm 2^{\circ}\text{C}$ , 240hrs	-
4	Low Temperature Storage	$-30^{\circ}\text{C}\pm 2^{\circ}\text{C}$ , 240hrs	-
5	High Temperature and High Humidity Operation Test	$60^{\circ}\text{C}\pm 2^{\circ}\text{C}$ , 90%, 240hrs.	-
6	Vibration Test	Total fixed amplitude: 1.5mm Vibration Frequency: 10~55Hz One cycle 60 seconds to 3 direction of X, Y, Z each 15 minutes.	-
7.	Drop Test	To be measured after dropping from 60cm high on the concrete surface in packing state. 	-

HANTRONIX, INC.  
10080 BUBB RD.  
CUPERTINO, CA 95014

Q.A.:  
Z.W.

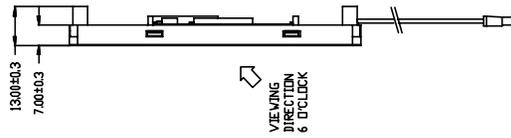
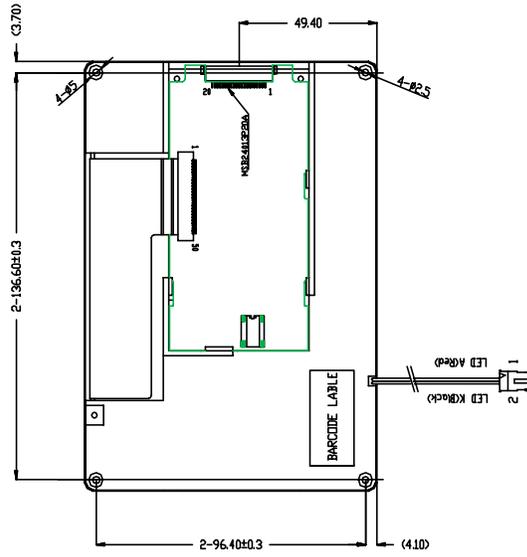
REV.:  
1.0

HDA570V-GHL

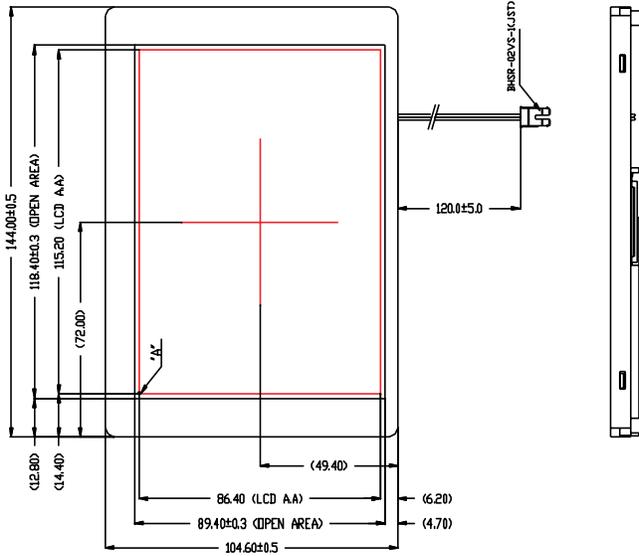
SHEET 18 OF 19

DATE:  
10/29/10

# 7 Dimensional Outlines



No.	PIN NAME	11	RIN2-
1	VCC	12	RIN2+
2	VCC	13	VSS
3	VSS	14	RCLK-
4	VSS	15	RCLK+
5	RIN0-	16	VSS
6	RIN0+	17	NC
7	VSS	18	NC
8	RIN1-	19	REV
9	RIN1+	20	VSS
10	VSS		



HANTRONIX, INC.  
10080 BUBB RD.  
CUPERTINO, CA 95014

Q.A.:  
Z.W.

REV.:  
1.0

HDA570V-GHL

SHEET 19 OF 19

DATE:  
10/29/10